

Patent Assignment Abstract of Title

Total Assignments: 1

Application #: 10014359 **Filing Dt:** 12/14/2001 **Patent #:** NONE **Issue Dt:**
PCT #: NONE **Publication #:** US20020080825 **Pub Dt:** 06/27/2002

Inventors: Michael Joachim Wolf, Werner Beisel, Jurgen Hohn

Title: Method and compensation module for the phase compensation of clock signals

Assignment: 1

Reel/Frame:	<u>012379</u> / <u>0160</u>	Received:	<u>12/21/2001</u>	Recorded:	<u>12/14/2001</u>	Mailed:	<u>02/11/2002</u>	Pages:	<u>2</u>
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Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

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<u>L7</u>	L6 and delay	9	<u>L7</u>
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<u>L2</u>	L1 and phase adj compensation near module	1	<u>L2</u>
<u>L1</u>	370/517.ccls.	439	<u>L1</u>

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